

Abstracts

A GaAs Double-Balanced Dual-Gate FET Mixer IC for UHF Receiver Front-End Applications (1985 [MWSYM])

K. Kanazawa, M. Kazumura, S. Nambu, G. Kano and I. Teramoto. "A GaAs Double-Balanced Dual-Gate FET Mixer IC for UHF Receiver Front-End Applications (1985 [MWSYM])." 1985 MTT-S International Microwave Symposium Digest 85.1 (1985 [MWSYM]): 60-62.

A double-balanced dual-gate FET mixer has been developed aiming at the application to a front-end circuit of the UHF receivers. An experimental IC has exhibited a 6 - 8 dB conversion gain without matching over a wide frequency range from 100 MHz to 800 MHz with the well suppressed RF/LO feedthrough by more than 20 dB and third-order intermodulation product of -60 dB.

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